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				Application Number	10/759489		
INFORM	ATION DIS	CLOS	URE	Filing Date	1/16/2004		
STATEM	ENT BY A	PPLIC/	ANT	First Named Inventor	Rodney E. Hooker		
				- Group Art Unit	2111		
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Sheet	1	of	1	Attorney Docket Number	CNTR.2233		

			U.S. PATENT	DOCUMENTS			
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Examiner	Cite	Foreign Patent Document	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear							
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CSK	ВА	"Dual On-Chip Instruction Cache Organization in High Speed Processors." IBM Technical Disclosure Bulletin, IBM Corp. New York, US. Volume 37, No. 12, December 1994, pages 213-214.	

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